



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 09/943,589 Confirmation No.:1967  
Applicant : Rainald Sander  
Filed : August 30, 2001  
Art Unit : 2829  
Examiner : Paresh H. Patel  
Title : Circuit Configuration for Detecting  
the Current in a Load Transistor  
Docket No. : WMP-IFT631  
Customer No. : 24131

ASSOCIATE POWER OF ATTORNEY

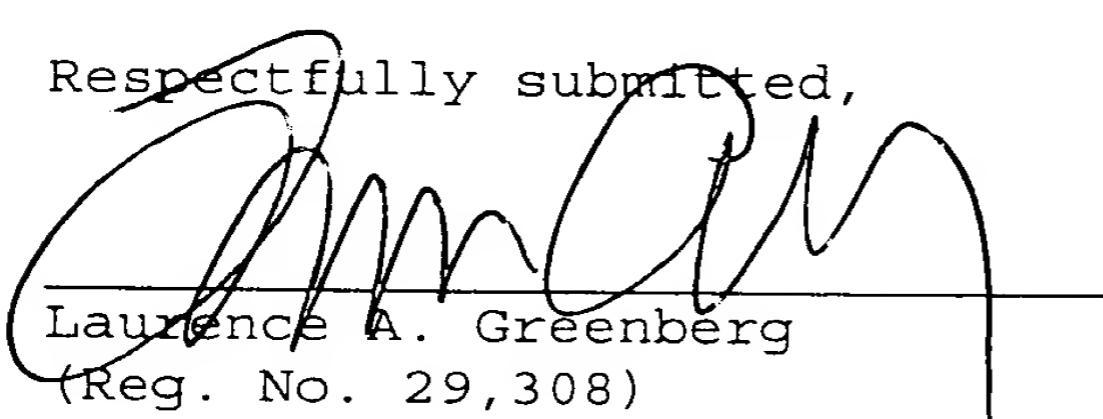
Hon. Commissioner of Patents,  
Washington, D. C. 20231

S i r :

Please recognize KYLE H. FLINDT (Reg. No. 42,539) as my  
associate in the matter of the above-identified  
application, with full powers. Please continue addressing  
all communications to the following address:

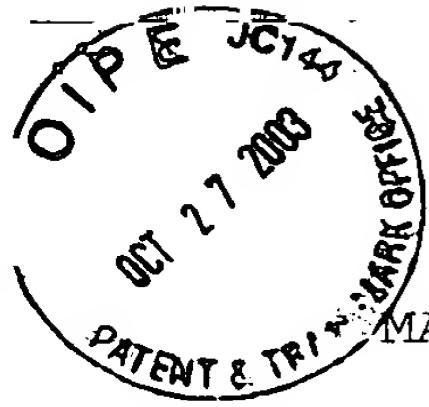
Lerner and Greenberg, P.A.  
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Hollywood, FL 33022-2480

Respectfully submitted,

  
Laurence A. Greenberg  
(Reg. No. 29,308)

October 20, 2003

Lerner and Greenberg, P.A.  
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MAS-FIN-172

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 09/951,823  
Applicant : Uta Gebauer et al.  
Filed : September 13, 2001  
TC/A.U. : 2831  
Examiner : Hung V Ngo

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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S i r :

Please recognize Mark P. Weichselbaum (Reg. No. 43,248) as my associate in the matter of the above-identified application, with full powers. Please continue addressing all communications to the following address:

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Respectfully submitted,

For Applicants

Date: October 22, 2003

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REG. NO. 41,947

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